



DR. SUDHIR SHELKE

	Nagpur,India,440013
	9765556956
	sudhirshelke1976@gmail.com

CAREER OBJECTIVE

Seeking a challenging and rewarding position in the Education and Research & Development field in good environment and want to contribute where I can effort my maximum for the organization. Given me an opportunity to prove myself where my knowledge can be shared, enriched with my academic.

EXPERIENCE

- Director, Administration, 08/07/2021 - Current
- Guru Nanak Institutions, Nagpur, Nagpur, India
- Professor, 16/05/2019 - Current
- Guru Nanak Institute of Technology, Nagpur, Nagpur, Maharashtra
- Principal (Regular –Ratified by UGC), 31/07/2019 - Current
- Guru Nanak Institute of Technology, Nagpur, Nagpur, Maharashtra
- Officiating Principal (Ratified by UGC), 29/06/2016 – 31/07/2019
- Guru Nanak Institute of Technology, Nagpur, Nagpur, Maharashtra
- Associate Professor (Ratified by UGC), 29/06/2016 – Current
- Guru Nanak Institute of Technology., Nagpur, Maharashtra
- HOD ETC /Designated Associate Professor, 25/04/2015 – 28/06/2016
- Guru Nanak Institute of Engineering & Technology, Nagpur, Maharashtra
- Vice Principal & HOD ETC / Designated Associate Professor, 01/01/2014 – 24/04/2015
- Central India College of Engineering & Technology, Nagpur, Maharashtra
- Asst. Professor & Head of Department, 12/05/2011 – 31/12/2013
- JD College of Engineering & Management, Nagpur, Maharashtra
- Asst. Professor (V Pay) & Head of Department, 17/11/2008 –

11/05/2011 Guru Nanak Institute of Engineering & Technology,
Nagpur, Maharashtra

- Lecturer, 02/06/2003 – 15/11/2008
- Guru Nanak Engineering College, Hyderabad., Nagpur, Maharashtra

-
- Asst. Professor, 01/10/2001 – 31/05/2003
 - Bapuji Engineering College, Nirmal., Nagpur, Maharashtra

-
- Software Engineer, 06/2000 - 09/2001
 - IICT VVW School, Nagpur., Nagpur, Maharashtra

EDUCATION

- Doctor of Philosophy (Ph.D.): Low Power Design Techniques and Physical Design Challenges.
- Ramdeobaba College of Engineering & Management, Nagpur, 2016
- University - Rashtra Sant Tukadoji Maharaj Nagpur University. Nagpur
- Date of Registration:10/01/2011
- Notification Detail RTMNU/Ph.D. (Cell/5/37 Dated 18 March 2016.)

-
- Master of Engineering (M.E.): Digital Electronics
 - Prof. Ram Meghe Institute of Research and Tech., Amravati, MH, 2007
 - Percentage: 67% First Division
 - Year of Passing: 2007

-
- Bachelor of Engineering (B.E.): Electronics Engineering Shri Guru Gobind Singhji College of Eng.& Tech., Nanded, MH, 2000
 - Percentage :66% First Division with Distinction
 - Year of Passing: 2000.

PORTFOLIOS

- Overall planning, development, renewal and changing of academic programs to ensure meaningful academic outcomes, quality, and effectiveness and obtaining the necessary approval
- Positioning the faculty with regard to research and teaching.
- Optimizing the through-put rate of students.
- Establishing a sound culture of research & innovation in the faculty.
- Plan and recruitment and training needs of academic and support staff.
- Consolidate and maintain high academic quality.
- Ensure the facilities such as labs, library and resource centers are of high quality and well-maintained.
- Endorse the appointment of academic advisors, external examiners, and associate member.
- Nurture the intellectual, emotional, and physical development of Students.

- Produce high quality and disciplined graduates of good character.
- Monitor and evaluate the performance and conduct of staff
- Foster collegial relationship and welfare among staff.
- Lead and plan staff development in line with objectives of university
- Ensuring the programs objectives are met with the allocated budget and resources available.
- Foster linkages with Institutions locally and nationally i.e., signed number of MoU's with different Industry.

GRANT RECEIVED

1. Title: Recent trends in Web Technology-Search Engine Optimization
Funding Agency: ISTE-AICTE
Amount in Rs:300000
Ref: ISTE /AICTE-ISTE FDP 1-3327974394/2018.
- 2.Title: 5G Technology
Funding Agency: ISTE-AICTE
Amount in Rs:93000
Ref: ISTE/AICTE-ISTE Induction-Refresher Program/2021-22
- 3.Title: Investigation of Pallet Prepared from different types of Organic Municipal Solid Waste – Alternate Fuel
Funding Agency: Rashtra Sant Tukadoji Maharaj Nagpur University (Nagpur, Maharashtra, IN)
Amount in Rs:160000
Ref: RTMNU/IIL/RGSTC/2023/652 DATE: 3RD MAY 2023
4. Title: Construction of Roundabout Using Waste Tires.
Funding Agency: Ministry of Micro, Small and Medium Enterprises (New Delhi, New Delhi, IN)
Amount in Rs:400000
Ref: File No.: 17(2)/ MSME Innovative/PMAC/2021-22
5. Title: Deen Dayal Upadhyaya - Grameen Kaushalya Yojana (DDU-GKY)
Funding Agency: Department of Rural Development and Panchayat Raj-Government of Maharashtra (Navi Mumbai, Maharashtra, IN)
Amount in Rs: 34,071,940
Ref: DDU-GKY/2023-24/160
6. Title: Unnat Bharat Abhiyan
Funding Agency: Ministry of Education, Government of India
Amount in Rs: 50000
- 7.Title: Higher Education Incubation (HEI) Centre
Funding Agency: Ministry of Micro, Small and Medium Enterprises (New Delhi, New Delhi, IN)
Amount in Rs: 1,00,0000
Ref: Order Sanction

**RESEARCH
SUPERVISOR/
EXAMINER**

1. Recognition as a Guiding/Supervising Ph.D. Scholar in the Subject Electronics Engineering. Under the faculty of Science & Technology. Reference No.: RTMNU/Ph.D. (Cell)/RRC/Guide/ Dated, 31/11/2020.
2. Recognition as a Supervisor for a M.E. By Research. in the Subject Electronics Engineering. Under the faculty of Engineering & Technology. RTM Nagpur University, Nagpur. Reference No.: Ph.D. (Cell)/RRC/Guide/56 Nagpur, Dated, 05/04/2014.
3. Recognition as a Co-supervisor for Ph.D. scholar Mr. Arun G. At shri. Satya Sai University of Technology and Medical Science, MP on dated 28/03/22. Reference No.: SSSUTMS/S/A/R/2020/05
4. Recognition as a Co-supervisor for Ph.D. scholar Mr. S. Mahesh Reddy At Shri. Satya Sai University of Technology and Medical Science, MP on dated 29/09/18. Reference No.: SSSUTMS/S/A/R/2018/06
5. External Examiner for Viva-Voce and Open Defense of Shri. Mayur D. Jakrede on 25-03-22 at Sant Gadage Baba Amaravati, University.
6. Recognition as a Co-supervisor for Ph. D scholar in the subject of Electronics & Telecommunication Engineering under the faculty of Science and Technology at GH Rasoni University, Amaravati. Reference No.: GHRUA/R&D Cell/Ph. D/Co-Sup/ETC005, dated- 15/03/21
7. Recognized as Co-supervisor for Ph. D Scholar Geetanjali P.Kale At G H Rasoni University Amravati MH on dated 01/03/2021. Ref No: GHRUA / R&D Cell/Ph.D./RRC/2020-21/007.

**NATIONAL/
INTERNATIONAL
JOURNALS**

1. Title: Proposing New Algorithm Based on UDF and SRIF for Image Centroid Tracking Cum Fusion (**SCOPUS**)
Journal Title: EFFLATOUNIA-Multidisciplinary Journal
Date/Month of Publication: 26/06/2021
ISSN: 0974-5823 Vol: 05(02)
URL: <https://efflatounia.com/index.php/journal/article/view/759>
2. Title: Design And Simulation of Optical Code Converters (**SCOPUS**)
Journal Title: Ilkogretim Online
Date/Month of Publication: 05/2021
DOI: 10.17051/ilkonline.2021.06.465. Vol: 20(6) Pp:7478-7493
URL: <https://doi.org/10.17051/ilkonline.2021.06.465>
3. Title: An Attempt for Comparing Different Techniques of Image Fusion (**SCOPUS**)
Journal Title: Ilkogretim Online
Date/Month/Year of Publication: 03/2021
ISSN: 4778-4793. Vol: 20(06)

DOI: doi: 10.17051/ilkonline.2021.06.465

[URL:https://www.bibliomed.org/mnsfulltext/218/218-1680112840.pdf?1700730923](https://www.bibliomed.org/mnsfulltext/218/218-1680112840.pdf?1700730923)

4. Title: Review on Linbo3 Based Eo-Mzi for Designing Optoelectronics Devices for Wdm Systems

Journal Title: Journal Of Critical Reviews

Date/Month of Publication: 07/12/2020

ISSN:2394-5125 Vol : 07(19) Pp:12566-12578

URL: <https://jcreview.com/admin/Uploads/Files/6443fb2005be77.86509214.pdf>

5.Title: GSM Based Electricity Theft Detection in Distribution Systems

Journal Title: Journal of Emerging Technologies and Innovative Research Date/Month of Publication: 03/2019 **(UGC)**

ISSN:2349-5162 Vol: 06(03) Pp:399-406

URL: <https://www.jetir.org/papers/JETIR1903B64.pdf>

6.Title: Automatic Attendance System using Biometric Sensor and IVRS **(UGC)**

Journal Title: IJSRSET

Date/Month of Publication: 10/03/2018

ISSN:2395-1990 Vol: 04(04) Pp:115-119

URL: <https://ijsrset.com/paper/3951.pdf>

7.Title: Low Power Design Methodology for 2-D Mesh NoC

Journal Title: Journal of Innovation in Electronics and Communication Engineering

Date/Month of Publication: 31/03/2018

ISSN:2455-1946 Vol: 07(02)

[URL:https://www.indianjournals.com/ijor.aspx?target=ijor:ijece1&volume=7&issue=2&article=010](https://www.indianjournals.com/ijor.aspx?target=ijor:ijece1&volume=7&issue=2&article=010)

08.Title: Design of FPGA Based Radix 4 FFT Processor using CORDIC
Journal Title: Journal of Innovation in Electronics and Communication Engineering

Date/Month of Publication: 27/06/2017

ISSN:2455-9946. Vol: 05(01) Pp:56-62

[URL:https://www.indianjournals.com/ijor.aspx?target=ijor:ijece1&volume=5&issue=1&article=011](https://www.indianjournals.com/ijor.aspx?target=ijor:ijece1&volume=5&issue=1&article=011)

9.Title: Secured Wireless Communication Using AES 256

Journal Title: Global Journal of Advanced Engineering Technology

Date/Month of Publication: 2014

ISSN:2277-6370 Vol: Special Issue (CTCNSF-2014) Pp:17-19

URL:

https://www.researchgate.net/publication/372768191_SECURED_WIRELESS_COMMUNICATION_USING_AES-256

10. Title: AES-256 Encryption in Communication using LabVIEW

Journal Title: International Journal of Advanced Research in Electrical,

Electronics and Instrumentation Engineering

Date/Month of Publication: 06/2015

ISSN:2278-8875 Vol: 04(06) Pp:5333-5340

DOI: 10.15662/ijareeie.2015.0406079

URL: http://www.ijareeie.com/upload/2015/june/79_AES.pdf

11.Title: Design of Digit Serial FIR Filter Using VHDL

Journal Title: IJISSET - International Journal of Innovative Science, Engineering & Technology

Date/Month of Publication: 03/2015

ISSN: 2348-1968 Vol: 02(03) Pp:821-825

URL: https://ijiset.com/vol2/v2s3/IJISSET_V2_I3_129.pdf

12. Title: Design and Implementation of Wishbone Bus Interface Architecture for SoC Integration USING VHDL ON FPGA

Journal Title: International Journal IJRITCC

Date/Month of Publication: 10/2014

ISSN: 2321-8169 Vol: 02(07) Pp:1847-1850

URL: <http://www.ijritcc.org>

13.Title: Low latency, area optimized, high through put double precision pipelined floating point multiplier using VHDL on FPGA

Journal Title: International IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE)

Date/Month of Publication: 06/2014

ISSN:2320-3331 Vol : 04(06) Pp:22-25

URL:<https://www.iosrjournals.org/iosr-jeee/Papers/ICAET-2014/electronics/volume-4/6.pdf?id=7590>

14.Title: FPGA based implementation of AES encryption and decryption with Verilog HDL

Journal Title: International Conference on Industrial Automation and Computing, ICIAC

Date/Month of Publication: 04/2014

URL:

https://scholar.google.com/citations?view_op=view_citation&hl=en&user=Sh5Jbw8AAAAJ&citation_for_view=Sh5Jbw8AAAAJ:Tyk-4Ss8FVUC

15.Title: Low-Latency, Low-Area Overhead and High Throughput NoC Architecture for FPGA Based Computing System (**IEEE-SCI**)

Journal Title: International Conference on Electronic Systems, Signal Processing and Computing Technologies-IEEEEXPLORE

Date/Month of Publication: 06/2014

ISBN:978-1-4799-2102-7

URL: <http://dx.doi.org/10.1109/icesc.2014.17>

16. Title: Low-Latency and Low- Area Over Head Based Performance Modelling of Network on Chip Architecture for FPGA Based Computing System.

Journal Title: International Journal of Computing, Communications and Networking (**SCOPUS**)

Date/Month of Publication: 12/2013

ISSN:2319-2720 Vol: 02(04) Pp:136-141

URL: <http://warse.org/pdfs/2013/ijccn09242013.pdf>

17. Title: Dynamic & Leakage Power Optimization Using Low Power Design Methodology - Gate Level power optimization for implementation of 2-D Mesh Network-On-Chip Router.

Journal Title: International Journal of Electrical & Electronics Engineering Research –Trans-Stellar Journal publication & Research Consultant

Date/Month of Publication: 03/2013

ISSN:2250-155X Vol: 03(01) Pp:147-160

URL:<https://www.tjprc.org/publishpapers/2-15-1359438654-16.Dynamic.full.pdf>

18. Title: Power and Area Efficient Router in 2- D Mesh Network-on-hip Using Low Power Methodology Clock Gating Techniques

(SCOPUS)

Journal Title: International Journal of Hybrid Information Technology

Date/Month of Publication: 07/2012

ISSN: 2005-4297 Vol: 05(03)

URL: https://gvpress.com/journals/IJHIT/vol5_no3/10.pdf

19. Title: Power and Area Efficient Router in 2-D Mesh Network-on-Chip Using Low Power Design RTL_Clock Gating Approach

Journal Title: Canadian Journal on Electrical and Electronics Engineering (**SCOPUS**)

Date/Month of Publication: 04/2012

Vol: 03(04) Pp:175-181

URL:https://scholar.google.com/citations?view_op=view_citation&hl=en&user=Sh5Jbw8AAAAJ&citation_for_view=Sh5Jbw8AAAAJ:u5HHmVD_uO8C

20. Title: Impact of SoC Low Power Design, Power Management Techniques & Physical Challenges

Journal Title: International Journal of Electrical, Electronics and Computing Technology (IJEECT)

Date/Month of Publication: 12/2012

ISSN: 2231-1009 Vol: 01(12) Pp:61-67

URL:<https://scholar.google.com/scholar?cluster=12657050021699272167&hl=en&oi=scholar>

21. Title: PCI-XP & High-Speed Interconnect. International Journal Computational Intelligence. Theory & Practice

Journal Title: IJCITP Date/Month of Publication: 2008, ISSN: 2231-1009 Vol: 01(12) Pp:61-67

22. Title: Design and Implementation of Wishbone Bus Interface

Architecture for SoC Integration USING VHDL ON FPGA

Journal Title: International Journal IJRITCC

Date/Month of Publication: 10/2014

ISSN: 2321-8169 Vol: 02(07) Pp:1847-1850

URL: <http://www.ijritcc.org>

23. Title: Design of Digit Serial FIR Filter Using VHDL

Journal Title: IJISSET - International Journal of Innovative Science, Engineering & Technology **(SCOPUS)**

Date/Month of Publication: 03/2015

ISSN: 2348-1968 Vol: 02(03) Pp:821-825

URL: https://ijiset.com/vol2/v2s3/IJISSET_V2_I3_129.pdf

24. Title: Digi Secure: Attribute-Based Document Transfer Solution

Journal Title: International Journal of INTELLIGENT SYSTEMS AND APPLICATIONS IN ENGINEERING **(SCI)**

Date/Month of Publication:16/08/2023

ISSN : 2147-6799 Vol : 11(10s) Pp: 840-848

URL: <https://ijisae.org/index.php/IJISAE/article/view/3337>

25. Title: Failure Mode and Effect Analysis of Transmission Case of a Tractor **(SCOPUS)**

Journal Title: International Journal of Mechanical Engineering - Copyrights @ Kolahari Journals-Scopus

Date/Month of Publication: 03/2022

ISSN : 0974-5823 Vol : 07 Pp: 1400-1406

URL: https://kalaharijournals.com/resources/SP%20Jan_Mar_170.pdf

26. Title: Proposing New Algorithm Based on UDF and SRIF for Image Centroid Tracking Cum Fusion **(SCOPUS)**

Journal Title: EFFLATOUNIA-Multidisciplinary Journal

Date/Month of Publication: 26/06/2021

ISSN : 0974-5823 Vol : 05(02)

**INTERNATIONAL
JOURNAL/
CONFERENCE
REVIEWER**

1. Conference / Journal Name: International conference on global Trends in Signal Processing, Information Computing and Communication
2. Organizer: SSBT's College of Engineering and Technology, Jalgaon, Maharashtra (Technically co-sponsored by IEEE) - Date: 2016/12.
3. Journal Name: International Journal of computer Science & Information Technology
4. Publisher: Institute of Scholar-InSc – SINCE YEAR 2019
5. Journal Name: International Journal of Emerging Trends in Engineering And Management. ISSN -2455-7993.- SINCE YEAR 2015
6. Journal Name: MICROELECTRONICS JOURNAL
7. Publisher: ELSEVIER-SCI - SINCE YEAR 2013

<p>INTERNATIONAL CONFERENCE AS SESSION CHAIR /KEY NOTE SPEAKER</p>	<ul style="list-style-type: none"> • 4th International Conference on Innovations in Electronics & Communications Engineering (ICIECE-2015) August 21-22, 2015 at Guru Nanak Institutions Technical Campus, Hyderabad. • World Innovation & Technology Expo & investment Summit 2023 during 10-12 Aug 2023 at Ghana South Africa under the Aegis of Pentagram Technologies, India & AKAKOSMOS Group, Ghana & UK.
<p>INTERNATIONAL CONFERENCE ORGANIZED AS A CONVENER / Co- CONVENER & CONFERENCE CHAIR</p>	<ol style="list-style-type: none"> 1. Conference Chair of International Conference on Scientific Computing 2. In Innovation, ICSCI 2022 at Guru Nanak Institutions, Nagpur 3. 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI 2014) In Technical Collaboration with IEEE Hyderabad Section 4. International Conference on Global Trends in Engineering & Management (ICGTEM 2013) at JD College of Engineering & Management.Nagpur.
<p>RESOURCE FACULTY</p>	<ol style="list-style-type: none"> 1. Resource Person for the one-day workshop on Research Methodology and How to Write Research paper organized by PG Department of Electronics and Communication Engineering, Abha Gaikwad Patil College of Engineering, Nagpur. Date 12 Sept. 2015 2. Key Note speaker at National conference at Sir Visvesvaraya Memorial Engineering College, Chincholi. Nashik, 3. Delivered expert talk on Micro-controller-based design in Workshop on Embedded System & Robotics ROBOCRAFT I at Guru Nanak Institute of 4. Engineering & Technology, Nagpur During 19th & 20th Sept. 2009.
<p>CONVENER OF NODAL CENTRE</p>	<ul style="list-style-type: none"> • Department of Electronics and Telecommunication Engineering, Gurunanak Institute of Engineering and Technology has been designated as Virtual-Lab Nodal Center for Nagpur Region by IIT-Bombay In association with Ministry of Human and Resource Development (MHRD) under IICT. Reference No. L/MP2/NC25/001 Dated August 18, 2015

**WORKSHOP
ORGANIZED
AS A
CONVENER**

1. One week ISTE Approved faculty Development Program (FDP) on Implementation of NEP & Green Construction in Guru Nanak Institute of Technology Nagpur during 19-23 Jun 2023.
2. One week ISTE Approved faculty Development Program (FDP) on Importance of Soft skills & NEP in Recent Scenario in Guru Nanak Institute of Technology Nagpur during 19-23 Jun 2023.
3. One week AICTE-ISTE Sponsored Induction /Refresher Program – 5G Technology in Guru Nanak Institute of Technology, Nagpur held during 21– 26 Feb 2022
4. One day workshop on Intellectual Property Rights in Guru Nanak Institute of Technology on 24 June 2022.
5. One day workshop on Intellectual Property Rights (IPR)- patent in Guru Nanak Institute of Technology, Nagpur jointly organized by Rajiv Gandhi national Institute of Intellectual Property Management (RGNIIPM, Govt. of India) on 26 Feb 2020.
6. One week Short Term training Program (STTP) on Innovative Technologies In Mechanical & Civil Engineering In Association with ISTE New Delhi in Guru Nanak Institute of Technology, Nagpur during 2 – 6 Mar 2020.
7. One week AICTE-ISTE sponsored Induction / Refresher Programme –Recent Trends in Web technology-Search Engine Optimization in Guru Nanak Institute of Technology held during 09 - 14 July 2018.
8. Three day workshop Technofun 2018 in Guru Nanak Institution Nagpur during 19 -21 Mar 2018.
9. Two week workshop on To Equip Faculty With Modern Day Techniques To achieve Excellence (EMDF) In Guru Nanak Institute of Technology Nagpur during 2-16 Jan 2016 conducted by Employability Quotient For Career EQ4C.
10. One week short term Training Program (STTP) on Emerging Trends in Mechanical Engineering in Guru Nanak Institute of Technology, Nagpur during 27-31 Dec 2016.
11. Lab VIEW and NI Hardware. In Association with ARAV Technologies. At Gurunanak Institute of Engineering and Technology, Nagpur 11 January 2016.
12. Virtual Lab In-House Faculty Training Programme. In Association with IIT Bombay. At Gurunanak Institute of Engineering and Technology, Nagpur 23 Oct. 2015.
13. Three Days Workshop on "VLSI Chip Designing Using VHIDL by Ni2 Logic Pvt. Ltd Pane at J D. College of Engineering & Management, Nagpur during 23 July - 25 July, 13.

14. One day Seminar on 'RF, Analog and Digital Chip / SOC Design by RF Sol Pvt Ltd, Nagpur at J D College of Engineering & Management, Nagpur on 16th July, 2013.
15. Two Days Workshop on Electronics Component Identification & Testing at J D College of Engineering & Management, Nagpur 23 July- 24 July, 13
16. Two Days Workshop on 'Front & Back End Design using VLSI Technology' by Ni2 Logic Pvt. Ltd, Pune at J D College of Engineering & Management, Nagpur during April 18-20, 2013.
17. Three Week winter Training Program on 'Advanced Industrial Automation by Prolic System pvt. Ltd. Nagpur at JD College of Engineering & Management, Nagpur during 4th Feb- 20th Feb 2013.
18. Three Week winter Training Program on Advanced Industrial Automation by Prolic System pvt. Ltd. Nagpur at JD College of Engineering & Management. Nagpur during 18th Dec- 10th Jan 2013.
19. One day Seminar on 'Industrial Automation by Prolic System pvt. Ltd. Nagpur at JD College of Engineering & Management, Nagpur on July 14, 2012
20. Three Days workshop on Digital VLSI System Design using FPGA at Central India College of Engineering and Technology during 19-21 Sept 2011.
21. Two day Workshop on Embedded Systems & Robotics ROBOCRAFT I' at Guru Nanak Institute of Engineering & Technology, Nagpur during 19th & 20th Sep. 2009
22. Two-day Workshop on Recent Trend Of MATLAB Software in Engineering & Technology at Guru Nanak, Institute of Engineering & Technology. Nagpur during 26th & 27th Nov, 2009.
23. One day workshop on Excellency in Engineering Teaching at Guru Nanak Institute of Engineering & Technology, Nagpur during 1st Aug 2009 Sponsored by ISTE
24. Two day Workshop on 'VLSI CHIP DESIGN at Guru Nanak Engineering College, Hyderabad During 4th & 5th Sep, 2008.

**SEMINAR/
WORKSHOP
ATTENDED**

1. One week Short Term training Program on VLSI to System Design: Silicon End Application Approach organized by AICTE & Arm Education during 31 July - 4 Aug 2023.
2. One day workshop on Intellectual Property Rights Organized by National Awareness Mission in GNIT, on 24 June 2022.
3. One day National Level workshop on Role of ICT in Education System: Opportunities and Challenges in Priyadarshani Bhagwati College of Engineering Nagpur on 27 May 2020.
4. One week online Short Term Training Program on Sensor, IoT & Machine Learning by K J Somaiya College of Engineering , Mumbai during 1-5 Jun 2020.
5. One week online Faculty Development Program on NAAC Awareness programme for faculty by Marathwada Mitra Mandal Institute of Technology (MMIT) Pune during 08-14 May 2020
6. One Week FDP on Digital VLSI Design & Verification in SB Jain Institute of Technology Management & Research Nagpur during 27-01 Jun 2019.
7. One day NBA Orientation workshop on outcome based accreditation process, and parameters Jointly organized by Gondwana University and LKM Institute of Management and Research on April 2017, Chandrapur.
8. Teacher Training Program on Task Based Training (TBT-2016) Conducted under Teacher Training through The e-Yantra Lab Setup Initiative (eLSI) by Department of CSE, IIT Bombay Poawi Mumbai.
9. Two days workshop on "Introduction to Robotics E-Yatra," at G.H. Rasoni College of Engineering, Nagpur In Association with IIT Bombay during 4-5 Dec 2015.
10. Two-day workshop on NBAABET 2015 Organized by Department Electronics and Communication Engineering. GNI Technical Campus School of Engineering and Technology Hyderabad 6-7 Nov 2015.
11. One Day workshop on Advanced Test & Measuring Equipments under International Conference on Electronics System, Signal Processing and Computing Technologies [ICESC-2014] at SRCOEM Nagpur during 9-11 Jan 2014.
12. The Pre-convention workshop on Higher Learning Through Research on 24 Oct, 2013 at Shri Ramdeobaba College of Engineering & Management, Nagpur.

13. Two Days workshop on ""IEEE activity "at S B Jain Institute of Technology, Management & Research, Nagpur during 5-6 Oct, 2013
14. Two day Workshop on "Recent Trend Of MATLAB Software in Engineering & Technology "at Guru Nanak Institute of Engineering & Technology, Nagpur during Design in 26-27 Nov, 2009.
15. Two day Workshop on "Embedded Systems & Robotics "ROBOCRAFT- I "at Guru Nanak Institute of Engineering & Technology, Nagpur during 19- 20 Sep, 2009.
16. One Day Workshop on "Excellency in Engineering Teaching "GuruNanak Institute of Engineering & Technology, Nagpur on 1st Aug, 2009. 8. Two day Workshop on "VLSI CHIP DESIGN "at Guru Nanak Engineering College, Hyderabad During 4-5 Sep. 2008.
17. Two Days Workshop on "Leadership Beyond the Boundary organized By NYSS Institute of management and Research, &YCCE Nagpur. During 12- 13 Feb 2008.
18. One Day workshop on "Robotics How to make a robot in a day "at Guru Nanak Engineering College, Hyderabad on 4th June 2007.
19. One-week STTP on" Digital System Design Using VLSI Techniques "Prof. Ram Meghe Institute of Technology and Research, Badnera, Amravati Maharashtra, during March 20- 24,2007

**JURY
MEMBERFOR
NATIONAL
SYMPOSIUM**

1. National Level Technical Paper Presentation at RKCOEM Nagpur. Feb.2015.
2. National conference on VLSI design, Embedded System & Signal Processing NCVDES-13 during 25-26 Sept 2013 at G H Raisonni College of Engineering Nagpur
3. National level Paper Presentation COGNIZIA-11 National Level Paper Presentation at SEMVC Chincholi Nashik during Mar2011.
4. National Level Technical Symposium SPANDAN 2010 The National Level Paper Presentation at YCCE, Nagpur on Feb 2010.
5. National Level Technical Synopsis with ELECROSURG2K6 In National Level Project Competition at MR MoU, Hyderabad, Jan 2006.

**PARTICIPATION
ON CURRICULUM
DESIGN**

1. One Day Workshop on "Guidance to Faculty for New Scheme of Syllabus of Eight Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Saint Vincent Palloti College of Engineering & Technology on 8 Dec. 2015.
2. One Day Workshop on "Guidance to Faculty for New Scheme of Syllabus of Sixth Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Central India College of Engineering & Technology on 30th January 2015
3. One day workshop on "Guidance to Faculty for New Scheme of Fifth Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Anjuman College of Engineering and Technology. on 18 September, 2014.
4. One day workshop on "Curriculum Development Workshop for BE 4th Semester Electronics and allied branches jointly organized by Board of studies, Rashtra Sant Tukadoji Maharaj Nagpur University and Jhulelal Institute of Technology on 18th October 2013
5. One day workshop on "Syllabus re framing of Electronics Electronics & Telecommunication Engineering Organized by RTM Nagpur University at SB. Jain Institute of Technology Management & Research on 1st Aug 2013.
6. One day workshop on Syllabus re Framing of Electronics & Electronics & Tele-Communication Engineering Organised by RTM Nagpur university at KDK College of Engineering Nagpur on 22 Jan 2013.
7. One day workshop on " Syllabus re Framing of Electronics / Electronics & Tele-Communication Engineering" Organised by RTM Nagpur university at J L Chaturvedi College of Engineering Nagpur 2012.
8. One day workshop on Syllabus" reformation of Faculty in Electronics Engineering" Organised by Board of Studies RTMNU, on 2 Dec 2011.
9. One day workshop on ""Curriculum Development on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Priyadarshani Bhagawati College of Engineering Nagpur on 11 September, 2011
10. One Day Workshop on "Guidance to Faculty for New Scheme of Syllabus of Eight Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Saint Vincent Palloti College of Engineering & Technology on 8 Dec. 2015.
11. One Day Workshop on "Guidance to Faculty for New Scheme of Syllabus of Sixth Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Central India College of Engineering & Technology on 30th January 2015

12. One day workshop on "Guidance to Faculty for New Scheme of Fifth Semester Syllabus "on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Anjuman College of Engineering and Technology. on 18 September, 2014.

13. One day workshop on "Curriculum Development Workshop for BE 4th Semester Electronics and allied branches jointly organized by Board of studies, Rashtra Sant Tukadoji Maharaj Nagpur University and Jhulelal Institute of Technology on 18thOctober 2013

14. One day workshop on "Syllabus re framing of Electronics Electronics & Telecommunication Engineering Organized by RTM Nagpur University at SB. Jain Institute of Technology Management & Research on 1st Aug 2013.

15. One day workshop on Syllabus re Framing of Electronics & Electronics &Tele-Communication Engineering Organised by RTM Nagpur university at KDK College of Engineering Nagpur on 22 Jan 2013.

16. One day workshop on" Syllabus re Framing of Electronics / Electronics& Tele-Communication Engineering" Organised by RTM Nagpur university at J L Chaturvedi College of Engineering Nagpur 2012.

17. One day workshop on Syllabus" reformation of Faculty in Electronics Engineering" Organised by Board Of Studies RTMNU, on 2 Dec 2011.

18. One day workshop on ""Curriculum Development on Behalf of BOS- Electronics Engineering RTMNU Nagpur at Priyadarshani Bhagawati College of Engineering Nagpur on 11 September, 2011
12.

**REFeree FOR
PAPER
PRESENTATION
CONTEST**

1. Worked as Referee for National Level Technical Paper Presentation Contest at S.B Jain Institute Technology, Management & Research. Nagpur

2. Worked as Referee for National Level Technical Paper Presentation Contest at G H Raisonni College of Engineering, Nagpur.

3. Worked as Referee for National Level Technical Paper Presentation Contest at Y C College of Engineering Nagpur.

4. Worked as Referee for National Level Technical Paper Presentation Contest at Sir Visvesvaraya Memorial Engineering College, Chin choli, Nashik.

**PATENTS
GRATED/
PUBLISHED**

1. Patent Title: ASIC BASED POWER AND AREA EFFICIENT ROUTER IN 2-D MESH NETWORK ON CHIP.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date:23-07-2024

Grant Number: 202021054427-545565

2. Patent Title: PHONE COVER BASED MOBILE CHARGERS.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date:22-12-2023

Grant Number: 397905-001

3. Patent Title: ORGANIC ELECTROLUMINESCENT DISPLAY DEVICE.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date:04-01-2024

Grant Number: 396928-001

4. Patent Title: SOLAR POWERED SUNSHADE CURTAIN FOR CARS

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date:20/08/2022

Grant Number: 369494-001 / 125403

5. Patent Title: DEVICE TO MONITOR THEFT BY IDENTIFYING SALIENT OBJECT

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date:18/04/2022

Grant Number: 362692-001 / 115529

6. Patent Title: Design and Fabrication of Integrated Mechanical Jack for four-wheeler.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date: 14/10/2022

Application _Number: 202221052951, Journal No.: 41/2022.

7. Patent Title: System and Broad-based framework for Environment audit of Industries.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date: 14/10/2022

Application _Number:202221052940, Journal No.: 41/2022.

8. Patent Title: Low power design Techniques for core.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date: 16/10/2022

Application _Number:202221052955, Journal No.: 41/2022.

9. Patent Title: ASIC Based Power and Area Efficient Router in 2-D Mesh Network on Chip.

Publisher: THE PATENT OFFICE - GOVERNMENT OF INDIA

Date: 15/01/2021

Application _Number:202021054427

AFFILIATION/A CCREDITATION	<ul style="list-style-type: none">▪ Successfully completed Process of program accreditation for NAAC.▪ External assessment of AICTE, DTE, University, NAAC and NBA, (in acycle as determined from time to time) of all departments.
PROJECT GUIDANCE	<ul style="list-style-type: none">▪ Ph.D.: - 03▪ M.Tech.: - 15▪ 3. B.E.: - 59
RESEARCH ARTICLE	Total Number of Publications: 53

**BOOKS
PUBLICATIONS**

Book(s)

1. Digital Image Processing authored by Dr. Sudhir N. Shelke & Dr. Pramod B. Patil- Vikas Publication House Pvt. Ltd, India. ISBN: 978-93-25984-33-
2. Basics of CMOS VLSI Design authored by Dr. Sudhir N. Shelke , Dr. Amol Deshmukh & Prof. Sridhar sahu – Narendra Publication, Nagpur, India. ISBN: 978-73-92572-47-0
3. Editor in Chief at ISTE Approved INTERNATIONAL CONFERENCE ON SCIENTIFIC COMPUTING IN INNOVATION (ICSCI 2022). ISBN: 978-93-91077-04-4 Organized by Guru Nanak Institutions, Nagpur.

**INTERNATIONAL
/ NATIONAL
CONFERENCE**

1. AES Implementation on Virtual Instrumentation.
 - Proceeding of 5th International Conference on Quality Up-gradation in Engineering Science and Technology, (IC-QUEST-2016)
 - Held at B. D. Deshmukh College of Engineering, Sevagram, Wardha, Nagpur. Pages 145-149 ISBN: 978-81-923623-1-1.
2. AES-Encryption in Secured Communication.
 - 4th International Conference on Innovations in Electronics & Communications Engineering (ICIECE-2015) August 21-22, 2015.
 - At Guru Nanak Institutions Technical Campus, Hyderabad.
3. Design of FPGA Based Radix-4 FFT Processors Using Cordic.
 - 4th International Conference on Innovations in Electronics & Communications Engineering (ICIECE-2015) August 21-22, 2015.
 - At Guru Nanak Institutions Technical Campus, Hyderabad.
4. AES-256 Implementation on Lab VIEW.
 - International conference on Science, Technology and Mathematics Education Trends & Alignment with Industry Needs.
 - At Tulsi Ramji Gaikwad Patil College of Engineering and Technology on 22-23 April, 2015.
5. Secured Wireless Communication Using AES-256.
 - National Conference on current trends in Computer Networks and its Framework. At VITS Hyderabad Dec. 2014.
6. Low-Latency, Low Area Overhead, and High Throughput NOC Architecture for FPGA Based Computing System.
 - Published In IEEE International Conference on Electronic Systems, Signal Processing and Computing Technologies

ICESC 2014.

<http://ieeexplore.ieee.org/xpl/abstractAuthors.jspr>

- Also available at Proceeding ICESC,14 International Conference on Electronic Systems, Signal Processing and Computing Technologies IEEE-Computer Society Washington, DC,USA ISBN:978-1-4799-2102-7,Pages(53,57),2014.doi10.1109/ICESC.2014.17http://dl.acm.org/citation.cfm?id=2586119.2587495&coll=DL&dl=GUIDE&CFID=472564605&CFTOKEN=66787751
 - Efficient NoC Architecture with CDMA Switch.
- 7. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Navigation Function for cooperative Target Tracking Robots.
- 8. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Low Latency Area Optimized, High Throughput Double Precision Pipe lined Floating Point Multiplier Using VHDL, OnFPGA
- 9. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (CSC1 2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Nios II Based System for the Playing of Wave Files on Cyclone IIFPGA.
- 10. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Multi Objective Optimization in Embedded System
- 11. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Automated Smart LPG Gas Cylinder Using GSM Technology
 - Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - VLSI Implementation of Wireless Sensor Network for Secured Communication.
- 12. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - Design and Implementation of HDLC Controller by Using Crc-16

13. Proceeding of 10th International Conference on Systemic Cybernetics & Informatics (ICSC1-2014)
 - In Technical Collaboration with IEEE Hyderabad Section.
 - ASIC, FPGA/CPLD Structure and Design.
14. Proceeding of 10th International Conference on Systemic Cybernetics & Informatics (ICSC1-2011)
 - In Technical Collaboration with IEEE Hyderabad Section
 - Smartphone using MEMS Accelerometers for Earthquake Detection
15. Proceeding of 10th International Conference on Systemic, Cybernetics & Informatics (ICSCI 2011)
 - 17. PCI XP A 3rd Generation I/O Interconnect.
16. Proceeding of International Conference on Systemic Cybernetics & Informatics (ICSCI 2008) Volume 1, page 355-361.
 - Hyderabad, India- January 2008
 - Design & Verification Of PCI-XP Transmit Buffer
17. Proceeding of International Conferences on Emerging Techniques in Computing, Electronics, Embedded System & VLSI Design (ICVED-2008)
 - Ahmednagar, India March 2008.
 - Microprocessor Based Relay System.
18. Proceeding of International conference 'RACE-07', Bikaner Engg. College, Bikaner.
 - Bikaner, India March 2008.
 - Design and Implementation PCI-XP Physical Layer Transmit/Receive Protocol.
19. Proceeding of IETE Zonal Seminar on Emerging And Converging Communication Technologies SECT-07
 - Vishakhapatnam, India Feb 2007
 - Design and Implementation PCI-XP Physical Layer Transmit Protocol.
20. Proceeding of IETE National Seminar on Information, Communication & Intelligent Systems
 - Cochin Dec 2007
 - 22. A Novel coding Technique for Low Power data Transmission
21. Proceeding of National conference, ETCIT2008
 - Amaravati. India Feb 2009
 - Proposal of Analysis of Schmitt Trigger at Low Voltages
22. Proceeding of National Conference L3C2010
 - Nagpur, India Apr 2010
 - Environmental Audit: A Paradigm Shift Towards Sustainable Development

23. Proceeding of ISTE Approved International Conference on Scientific Computing in Innovation, ICSCI, 2022, Page 182-189, ISBN: 978-93-91077-04-4. April 6-2022, Nagpur

- ZIGBEE live Electricity Metre Monitoring with Tempering Proof

24. Proceeding on National Conference on Emerging Technologies in computer science and Engineering, Nagpur, January 20-2017.

- Methyl Ester of Jojoba oil (*Smmondsia chinensis*) and Its Selected blends as diesel Substitute: A Study on Performance and Exhaust Emissions in VCR Diesel Engine

25. Proceeding of ISTE Approved International Conference on Scientific Computing in Innovation, ICSCI, 2022, Page 182-189, ISBN: 978-93-91077-04-4. April 6-2022, Nagpur

- Use of Chain Rule, activation Functions, perceptions in back propagation Algorithms of Machine Learning and Deep Learning

26. Proceeding of ISTE Approved International Conference on Scientific Computing in Innovation, ICSCI, 2022, Page 182-189, ISBN: 978-93-91077-04-4. April 6-2022, Nagpur

- Environmental Audit: Paradigm Shift Towards Sustainable Development.

27. Proceeding of ISTE Approved International Conference on Scientific Computing in Innovation, ICSCI, 2022, Page 182-189, ISBN: 978-93-91077-04-4. April 6-2022, Nagpur.

MEMBERSHIP

1. ISTE (Life Time) Membership: LM39196
2. INSC Institute of Scholars: InSc20200316
3. EduSkills Membership: 052006210307624

AWARDS

1. Title of the Awards: BEST FACULTY CHAPTER (ENGINEERING)-GURU NANAK INSTITUTE OF TECHNOLOGY

Agency: Indian Society for Technical Education, New Delhi

Year/Month/Date: 2022/10

2. Title of the Awards: IEI - PARYAVARAN SAATHI AWARD 2023

Agency: Institution of Engineers (India)

Year/Month/Date: 2023/06

3. Title of the Awards: CLIMATE LITERACY LEADERSHIP AWARD – 2022

Agency: CENTER FOR CREATIVITY AND INNOVATION, Nagpur, MH

Year/Month/Date: 2022/08

4. Title of the Awards: Spirit of Humanity -Educational Socio Welfare Award

Agency: Prerana Multi Purpose Society, Nagpur MH

Year/Month/Date: 2022/04

5. Title of the Awards: BEST PRINCIPAL AWARD – 2022
 Agency: Regent High School , kalmeshwar, Nagpur MH
 Year/Month/Date: 2022/01

6. Title of the Awards: EDUCATIONAL EXCELLENCE AWARD- 2021
 SPECIAL AWARD
 Agency: BEGINUP RESERACH INTELLIGENCE PRIVATE LIMITED @ TAJ
 WEST END BANGALURU
 Year/Month/Date: 2021/09

7. Title of the Awards: SWACCHA COLLEGE AWARD- GURU NANAK
 INSTITUTE OF TECHNOLOGY
 Agency: Regent High school kalmeshwar, Nagpur , MH
 Year/Month/Date: 2021/12

8. Title of the Awards: OUTSTANDING LEADERSHIP PRINCIPAL OF THE
 YEAR – 2021
 Agency: ASIAN EDUCATION AWARD CONFERENCE ORGNIZED BY KITES
 PRODUCTION, New Delhi.
 Year/Month/Date: 2021/05

9. Title of the Awards: BEST PRINCIPAL AWARD -2020
 Agency: INSTITUTE OF SCHOLAR, InSC, New Delhi.
 Year/Month/Date: 2020/02

10. Title of the Awards: INNOVATIONPARV 2019
 Agency: MAYOR INNOVATION COUNCIL, Nagpur, MH.
 Year/Month/Date: 2019/09

10. Title of the Awards: OUTSTANDING A ACADEMICIAN
 Agency: Pentagonam research Centre (p) Limited, India
 Year/Month/Date: 2014/02/03

11. Title of the Awards: BEST PAPER AWARD
 Agency: International Conference on Systemics, Cybernetics and
 Informatics ICSCI—2008, Hyderabad, Andhra Pradesh.
 Year/Month/Date: 2008/01

**UNIVERSITY
 COMMITTEE
 MEMBER**

1. Member appointed by Vice Chancellor of the Affiliation Committee on Continuation of Affiliation of various Engineering Institutions under RTM Nagpur University, Nagpur.
2. Approved teacher for moderating, setting & evaluation of papers for the PG & UG university examination papers at RTM Nagpur University.
3. Paper setter for PG at G H Rasoni College of Engineering an Autonomous Institution Nagpur
4. Paper setter for PG at Y C College of Engineering an autonomous Institution Nagpur.

**PERSONAL
DETAILS**

- Full Name: Sudhir N. Shelke
- Father Name: Dr. Namdeorao Shelke
- Spouse: Pratibha S. Shelke, Additional Executive Engineer, KTPS Nagpur.
- Nationality: Indian
- Date of Birth: June 24,1976
- Permanent & Residential Address: House No. 28, Khan Society, AdarshColony, Katol Road, Nagpur (MS)-440013, India
- PAN No: AEIPN9235Q
- PASSPORT No: V 1840671
- Profile Links:
- Google scholar:
<https://scholar.google.com/citations?user=Sh5Jbw8AAAAJ&hl=en>
- ORCID: <https://orcid.org/0000-0002-5125-5228>
- Academia.edu:
<https://gurunanakinstitutions.academia.edu/SudhirShelke>
- Research Gate : <https://www.researchgate.net/profile/Sudhir-Shelke>
- Semantic Scholar : <https://www.semanticscholar.org/author/S.-Shelke/37011524>

Dr. Sudhir N. Shelke